

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
1	BRS	L1	54643	power near5 (processor microprocessor cpu.mpu)	US-PGPUB; USPAT	2005/03/21 20:36		
2	BRS	L2	31321	(stop\$4 sleep) same clock	US-PGPUB; USPAT	2005/03/21 20:37		
3	BRS	L3	10591	(low\$2 near5 power) same clock	US-PGPUB; USPAT	2005/03/21 20:38		
4	BRS	L4	1228	1 same 2	US-PGPUB; USPAT	2005/03/21 20:38		
5	BRS	L5	1307	1 same 3	US-PGPUB; USPAT	2005/03/21 20:38		
6	BRS	L6	1958	4 or 5	US-PGPUB; USPAT	2005/03/21 20:39		
7	BRS	L7	440226	(input\$4 receiv\$3) near3 data	US-PGPUB; USPAT	2005/03/21 20:42		
8	BRS	L8	1100	6 and 7	US-PGPUB; USPAT	2005/03/21 20:43		
9	BRS	L9	250834	(start\$3 wak\$3 initiat\$3) near5 process\$3	US-PGPUB; USPAT	2005/03/21 20:45		
10	BRS	L10	518	8 and 9	US-PGPUB; USPAT	2005/03/21 20:49		
11	BRS	L11	513	10 and (state mode)	US-PGPUB; USPAT	2005/03/21 20:51		
12	BRS	L12	74604	clock near5 frequency	US-PGPUB; USPAT	2005/03/21 20:51		
13	BRS	L13	284	11 and 12	US-PGPUB; USPAT	2005/03/21 20:52		
14	BRS	L14	2396	713/300-320.ccls.	US-PGPUB; USPAT	2005/03/21 20:52		
15	BRS	L15	1428	713/322-324.ccls.	US-PGPUB; USPAT	2005/03/21 20:53		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
16	BRS	L16	333	713/601.ccls.	US-PGPUB; USPAT	2005/03/21 20:53		
17	BRS	L17	3553	14 or 15 or 16	US-PGPUB; USPAT	2005/03/21 20:54		
18	BRS	L18	105	13 and 17	US-PGPUB; USPAT	2005/03/21 20:54		